

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re application of: Yuka HAYAMI, et al.

Serial Number: Not Yet Assigned

Filed: October 30, 2003

**Customer No.: 38834**

For: SEMICONDUCTOR DEVICE FABRICATION METHOD

**INFORMATION DISCLOSURE STATEMENT**

Commissioner for Patents  
P. O. Box 1450  
Alexandria, VA 22313-1450

October 30, 2003

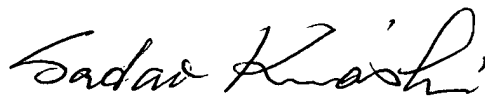
Sir:

In compliance with 37 CFR 1.56, Applicant(s) call(s) to the attention of the Patent and Trademark Office the references listed on the attached PTO-1449.

A copy of each of the references is enclosed herewith.

In the event there are any fees due in connection with the filing of this paper, please charge Deposit Account No. 50-2866.

Respectfully submitted,  
WESTERMAN, HATTORI, DANIELS & ADRIAN, LLP



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Enclosures: PTO-1449; References(2)

<b>INFORMATION DISCLOSURE STATEMENT PTO-1449</b>	Atty. Docket No. 032070	Serial No. New Appln.
	Applicant(s): Yuka HAYAMI, et al.	
	Filing Date: October 30, 2003	Group Art Unit:

### U.S. PATENT DOCUMENTS

Examiner Initial	Document No.	Name	Date	Class	Subclass	Filing Date (If appropriate)
_____	AA					
_____	AB					
_____	AC					
_____	AD					

### FOREIGN PATENT DOCUMENTS

Document No.	Date	Country	Translation (Yes or No)
_____ AE 6-209081	07/26/94	Japan	Abstract & Discussed in the Spec.
_____ AF 6-204243	07/22/94	Japan	Abstract & Discussed in the Spec.
_____ AG			
_____ AH			
_____ AI			

### OTHER DOCUMENTS

_____ AJ	
_____ AK	
Examiner	Date Considered